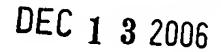
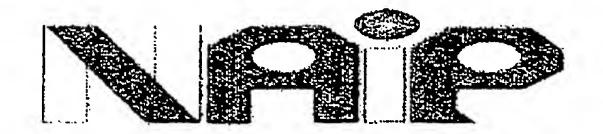
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From: Winston Hsu, Registration No. 41,526

Serial No.: 10/710,934 Patent No.: 7,075,375

Attorney Docket No.:MTKP0173USA

Subject: Citation of Prior Art under 37 CFR §1.501.

Total Pages: 3 Zpages (including cover page)

Winston Hsu

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MTKP0173USA0 D2 1

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Firm Name  North America Intellectual Property Corporation  Signature  Printed name  Winston Hsu  Date  DEC. 1 3 2008  Reg. No. 41,526  CERTIFICATE OF TRANSMISSION/MAILING  hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:  Signature  Toc I fluang	Fee Attached  Amendment/Repty  After Final  Aftidavits/declaration(s)  Extension of Time Request  Express Abandonment Request  Information Disclosure Statement  Certified Copy of Priority Document(s)  Repty to Missing Parts/ Incomplete Application  Repty to Missing Parts		Drawing(s)  Licensing-related Papers  Petition  Petition to Convert to a  Provisional Application  Power of Attorney, Revocation  Change of Correspondence A  Terminal Disclaimer  Request for Reland  CD, Number of CD(s)  Landscape Table on CD	i ddress	After Apper of Ap Apper of Ap Apper (Apper Apper (Apper Deformance) Statue Cother belov	eal Communication to Boa speals and interferences at Communication to TC sat Notice, Brief, Reply Brief detary Information s Letter r Enclosure(s) (please kie v);	rd n
North America Intellectual Property Corporation  Signature  Winston Hsu  Date  DEC. 1 3 2008  Reg. No. 41,526  CERTIFICATE OF TRANSMISSION/MAILING  Thereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:  Signature  Toe Human	SIGN	ATURE	OF APPLICANT, ATTOR	RNEY, O	RAGENT	^^^\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	·
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37 CFR 1.501 INFORMATION DISCLOSURE CITATION			Docket Number (Opt ATTK PRITEUSA	Patent Number 7.075.375				
			Applicant MEDIATEK INCORPORATION					
IN A PATENT (Sheet;of)				tssue Date 07/11/2006	Art Unit 2817			
			U.S. PATE	NT DOCUMENTS			بيراماني المالية المالية المالية المالية المالية	أكبار أغرين فالمروعية بالمطالبة عرادية
examiner initial	DOCUMENT NUMBER	DATE		NAME CLASS			SUBCLASS FILING DA	
	2001/0055249	12/27/3889	Moriwaki, Isamu		GHB	7/0045	04/24/2001	
	2003/0067356	(METRY,1003)	Bokui, Takahiro; et al.		1403L.	7/00	08/08/2002	
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#### PATENT

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Hong-Ching Chen, et al.

5 Patentee: MEDIATER INCORPORATION

Appl. No.: 10/710,934 Filing Date: 08/13/2004

Docket No.: MTKP0173USA Confirmation No.: 4933

Title: PHASE LOCKED LOOP FOR CONTROLLING AN OPTICAL

10 RECORDING DEVICE AND METHOD THEREOF

To: Commissioner for Patents

P.O. BOX 1450

Alexandria, VA 22313-1450

15

Subject: Submission of Prior Art under 37 CFR \$1.501

Dear Sir,

20

25

The undersigned agent hereby submits the prior arts as cited in the Office Action issued on 10/13/2006 against the counterpart CHINA patent application no. 2005100535318. A form PTO/SB/42 detailing prior arts cited, copies of the patents or printed publications, and any necessary English translation are attached

pursuant to 37 CFR 1.501 and MPEP 2205.

Office Action are pertinent and applicable to the patentability of the claims of the above-identified patent. It is the patentee's desire to have the citation of prior art placed in the patent file.

10 Respectfully submitted,

Wentenbar

Date: DEC. 1 3 2006

Winston Hsu, Patent Agent No. 41,526

15 P.O. BOX 506, Merrifield, VA 22116, U.S.A.

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HASE LOCKED LOOP for CONTROLLING RECORDABLE OPTICAL DISK DRIVE

A phase locked loop (PLL) for generating an oscillation signal such that the oscillation signal maintains a substantially constant phase difference with respect to a phase of a reference signal. The PLL includes a first frequency divider for dividing a frequency of the oscillation signal by a first divisor, and for generating a number of multiphase signals, such that each multiphase signal is out of phase with each of the other multiphase signals, a phase shift detection circuit for comparing a phase of the oscillation signal with the phase of the reference signal, and a phase adjusting circuit for selecting one of the multiphase signals generated by the first frequency divider to be an output signal such that the PLL is able to reduce the phase difference between the oscillation signal and the reference signal.